



IN THE ENITED STATES PATENT AND TRADEMARK OFFICE

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Filing Date	May 6, 1997
Inventor	Sharad Mathur et al.
Group Art Unit	
Examiner	S.XLao
Attorney's Docket No.	MS1-151US
Confirmation No	
Title: Controlling Memory Usage in Systems Having Limited Physical Memory	

RESPONSE TO OFFICE ACTION DATED DECEMBER 30, 2003

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AMENDMENTS

Amendments to the claims begin on page 2 of this paper.

New claims begin on page 20 of this paper.

Remarks/Arguments begin on page 23 of this paper.

Allowed subject matter is noted on page 23 of this paper.

Art rejections begin on page 24 of this paper.

Traverse of rejection I under 35 U.S.C. §103 begins on page 25 of this paper.

Traverse of rejection II under 35 U.S.C. §103 begins on page 40 of this paper.

Traverse of rejection III under 35 U.S.C. §103 begins on page 45 of this paper.

Traverse of rejection IV under 35 U.S.C. §103 begins on page 48 of this paper.

Deficiencies in Examination begins on page 52 of this paper.

A conclusion appears at page 54 of this paper.

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